

SPREAD SPECTRUM RECEIVERABSTRACT OF THE DISCLOSURE

5 The spread spectrum receiver employs circuit 21, 31,
etc. based on direct conversion techniques. These
circuits enable realization of spread spectrum receivers
of greatly reduced complexity and of much higher chip
rates than can be implemented with the standard approach
10 of a fully digital receiver. With these circuits, the
digital processing at the receiver is performed at the
data symbol rate and not at a multiple of the chip rate
that is customary in state-of-the art spread spectrum
and CDMA receiver design.